

Syntax and Operational Semantics of TAL-0

Software Security

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The material in these notes has been adapted from Section 4 of [1].

Syntax

The syntax of TAL-0 is parameterised over a natural number $k \in \mathbb{N}$ (the number of registers) and a set \mathcal{L} of *labels*, and is given in Figure 1.

Registers r as in:	
$r ::= r_1 \mid r_2 \mid \dots \mid r_k$	
Operands v as in:	
$v ::= n \in \mathbb{Z}$	(integer literals)
$\mid l \in \mathcal{L}$	(labels)
$\mid r$	(registers)
Instructions ι as in:	
$\iota ::= r_d := v$	(move)
$\mid r_d := r_s + v$	(add)
$\mid \text{if } r \text{ jump } v$	(conditional jump)
Instruction sequences I as in:	
$I ::= \text{jump } v$	(jump)
$\mid \iota ; I$	(sequence)

Figure 1: The Syntax of the TAL-0 Language

Let \mathcal{O} be the set of all operands v in TAL-0, and let \mathcal{S} be the set of all

instruction sequences I . For example:

$$\begin{aligned} 3 \in \mathcal{O} \quad \text{home} \in \mathcal{L} \Rightarrow \text{home} \in \mathcal{O} \quad x \leq k \Rightarrow r_x \in \mathcal{O} \\ v \in \mathcal{O} \Rightarrow \text{jump } v \in \mathcal{S} \quad x, y, v \in \mathcal{O} \Rightarrow r_x := r_y + v; \text{jump } v \in \mathcal{S} \\ \text{jump } 3 \in \mathcal{S} \end{aligned}$$

Operational Semantics

TAL-0 programs are evaluated with respect to a *register valuation*, which is a function:

$$R : \{1, \dots, k\} \rightarrow \mathcal{O}$$

together with a *heap*, which is a (finitely-supported) partial function:

$$H : \mathcal{L} \rightarrow \mathcal{S}$$

Every heap can be written as a (finite) set of pairs (l, I) with $l \in \mathcal{L}$ and $I \in \mathcal{S}$, subject to the restriction that each $l \in \mathcal{L}$ appears in at most one such pair. For example, if $\text{one}, \text{two}, \text{three} \in \mathcal{L}$ and we define:

$$H = \{(\text{one}, \text{jump } 2), (\text{two}, \text{jump } 3), (\text{three}, r_1 := r_2 + 1; \text{jump } 4)\}$$

then we have:

$$H(\text{one}) = \text{jump } 2 \quad H(\text{two}) = \text{jump } 3 \quad H(\text{three}) = r_1 := r_2 + 1; \text{jump } 4,$$

with $H(l)$ undefined for any other $l \in \mathcal{L}$.

If H is a finitely supported partial function then for any $a \in \mathcal{L}$ and $X \in \mathcal{S}$ we may define another heap $H[(a, X)]$ as in:

$$H[(a, X)](l) = \begin{cases} X & \text{if } a = l \\ H(l) & \text{otherwise} \end{cases}$$

The operational semantics of TAL-0 is given by an abstract machine. A *state* of the abstract machine is a 3-tuple (H, R, I) where:

- $H : \mathcal{L} \rightarrow \mathcal{S}$ is a heap
- $R : \{1, \dots, k\} \rightarrow \mathcal{O}$ is a register valuation
- $I \in \mathcal{S}$ is an instruction sequence

We define a function $\hat{R} : \mathcal{O} \rightarrow \mathcal{O}$ as in:

$$\hat{R}(v) = \begin{cases} R(i) & \text{if } v = r_i \\ n & \text{if } v = n \in \mathbb{Z} \\ l & \text{if } v = l \in \mathcal{L} \end{cases}$$

and we define a partial function $\widehat{H} : \mathcal{O} \rightarrow \mathcal{S}$ as in:

$$\widehat{H}(v) = \begin{cases} H(v) & \text{if } v = l \in \mathcal{L} \\ \uparrow & \text{otherwise} \end{cases}$$

Now the transition rules for our abstract machine are as in Figure 2.

$$\boxed{\begin{array}{c} \frac{\widehat{H}(\widehat{R}(v)) = I}{(H, R, \text{jump } v) \rightarrow (H, R, I)} \text{ JUMP} \\[10pt] \frac{}{(H, R, r_d := v; I) \rightarrow (H, R[(r_d, \widehat{R}(v))], I)} \text{ MOVE} \\[10pt] \frac{R(r_s) = n_1 \in \mathbb{Z} \quad \widehat{R}(v) = n_2 \in \mathbb{Z}}{(H, R, r_d := r_s + v; I) \rightarrow (H, R[(r_d, n_1 + n_2)], I)} \text{ ADD} \\[10pt] \frac{R(r) = 0 \quad \widehat{H}(\widehat{R}(v)) = J}{(H, R, \text{if } r \text{ jump } v; I) \rightarrow (H, R, J)} \text{ COND-1} \\[10pt] \frac{R(r) \neq 0}{(H, R, \text{if } r \text{ jump } v; I) \rightarrow (H, R, I)} \text{ COND-2} \end{array}}$$

Figure 2: Transition rules for the TAL-0 abstract machine

It is convenient to specify heaps as in:

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prod: r3 := 0;                                // result = 0
      jump loop

loop: if r1 jump done;                        // if a = 0 we are done
      r3 := r2 + r3;                          // result := result + b
      r1 := r1 + (-1);                        // a := a - 1
      jump loop

done: jump r4                                // return

```

where, ignoring the comments for a moment, this corresponds to the heap:

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{(prod, r3 := 0; jump loop)
, (loop, if r1 jump done; r3 := r2 + r3; r1 := r1 + (-1); jump loop)
, (done, jump r4)}

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Now, let H be the above heap, and suppose that $R_0(r_1) = 2$, $R_0(r_2) = 2$, and $R_0(r_4) = \text{exit} \in \mathcal{L}$. Then we have:

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(H, R0, jump prod)
→(H, R0, r3 := 0, jump loop)
→(H, R0[(r3, 0)], jump loop)
→(H, R0[(r3, 0)], if r1 jump done; r3 := r2 + r3; r1 := r1 + (-1); jump loop)
→(H, R0[(r3, 0)], r3 := r2 + r3; r1 + (-1); jump loop)
→(H, R0[(r3, 2)], r1 + (-1); jump loop)
→(H, R0[(r3, 2), (r1, 1)], jump loop)
→(H, R0[(r3, 2), (r1, 1)], if r1 jump done; r3 := r2 + r3; r1 := r1 + (-1); jump loop)
→(H, R0[(r3, 2), (r1, 1)], r3 := r2 + r3; r1 := r1 + (-1); jump loop)
→(H, R0[(r3, 4), (r1, 1)], r1 := r1 + (-1); jump loop)
→(H, R0[(r3, 4), (r1, 0)], jump loop)
→(H, R0[(r3, 4), (r1, 0)], if r1 jump done; r3 := r2 + r3; r1 := r1 + (-1); jump loop)
→(H, R0[(r3, 4), (r1, 0)], jump r4)

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In this way, the program encoded by our heap allows us to multiply the value stored in r_1 by the value stored in r_2 , with the result ending up in r_3 . Once finished, our program jumps to the label $\text{exit} \in \mathcal{L}$ which we assume is stored in r_4 .

Exercises

- Write a program (specified as a heap, as in the example above) that computes the factorial $n!$ of a positive integer $0 < n \in \mathbb{Z}$.
- Test your program by showing that it successfully computes $3!$ when run using the abstract machine. This should look something like computation above, in which we show that our program successfully computes the product of 2 and 2.
- Write a program that causes the machine to “get stuck”, in the sense that the antecedent of a the relevant transition rule of the abstract machine is not satisfied.

References

- [1] Pierce, B.C. *Advanced Topics in Types and Programming Languages*. MIT Press, 2004.