

Transforming System Views in Software Design

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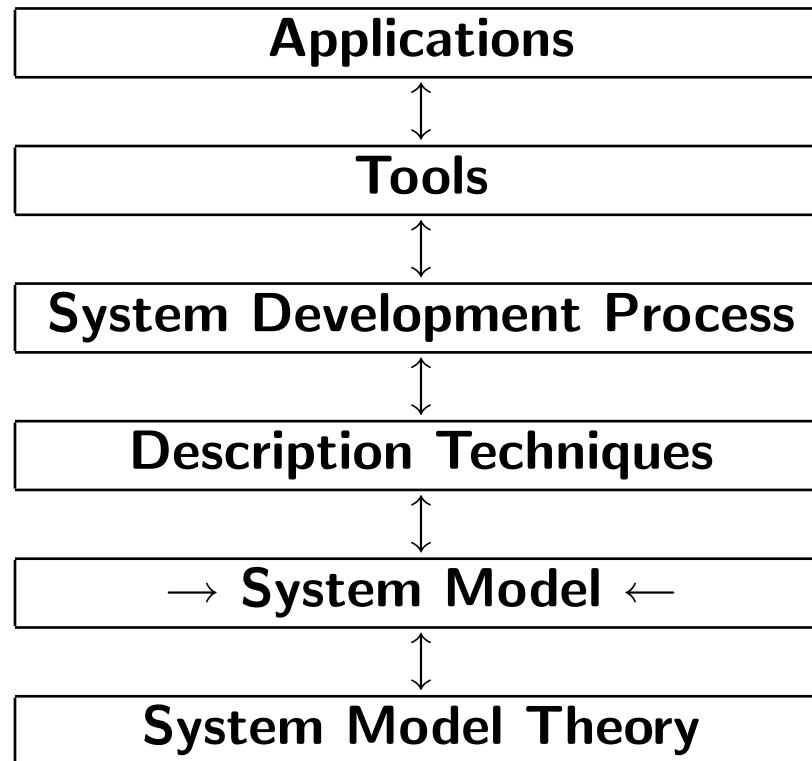
1. Introduction — IT Trends

- *Computing power and performance increase.*
- *Multi-functionality*
- *Interoperability*
- *Connectivity*
- *Dependability (safety and security)*
- *Reusability of Solutions*
- *(Quasi) Standards*
- *Computing Paradigms*
- *Software Engineering — Software Industry*

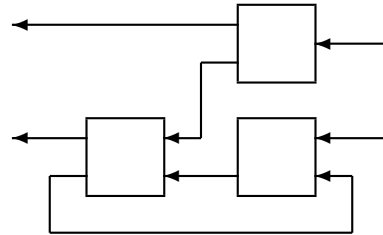
... Scientific foundations of software technology?



1. Introduction — Layers of Software Technology



1. Introduction — System Model



- **Distributed systems** are networks of *components*.
- Components **communicate asynchronously** via *unidirectional channels*.
- **Application areas**: processing — memory — transmission – control.
- **System model** uses *streams* to record *communication histories*.
- A **stream processing function** describes a component's *I/O behaviour*.
- A **state transition system** describes a component's **implementation**.

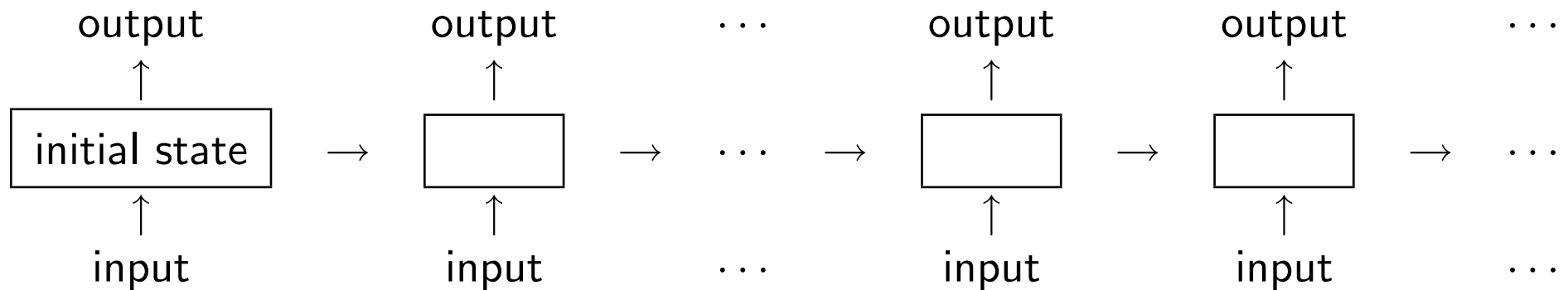


1. Introduction — Models of Computation

Classical Model of Computation



Model of Computation for Interactive Systems



1. Introduction — Components

Ingredients for a Component Engineering Framework

- (Uniform) notion of **component**
 - *Syntactic interface*
 - *Semantic interface* $\hat{=}$ *(input/output) behaviour*
- **Specification**
- **Stepwise Refinement**
 - *Property refinement*
 - *Decomposition — Architecture*
 - *Implementation*
- **Compositionality** — Architecture
- **System views** on different **levels of abstraction**

... Graphical notation



1. Introduction — Views of a Component

Data Structure	Communication-Based Description	State-Based Description	Trace-Based Description
<i>Data Model</i>	<i>Black-box View</i>	<i>Glass-box View</i>	<i>Process View</i>

UML

- *Class diagrams* ↔ data structure
- *State machines* ↔ state-based description
- *Sequence diagrams* ← communication-based description



2. Data Model

The **data model** describes the *data structure* underlying a component.

- **Signature** $\Sigma = (T, F)$
 - T is a set of *types*.
 - F is a family of *function symbols* $f : t_1 \star t_2 \star \dots \star t_n \rightarrow t_{n+1} \quad (n \geq 0)$.
- Σ -**Equations** $\forall x : t \text{ term}_1 = \text{term}_2$
- **Abstract Data Type** (Σ, E)
- A **data model** \mathcal{A} is a Σ -algebra providing
 - a *carrier set* $t^{\mathcal{A}}$ carrier set for each type $t \in T$,
 - a *function* $f^{\mathcal{A}} : t_1^{\mathcal{A}} \times t_2^{\mathcal{A}} \times \dots \times t_n^{\mathcal{A}} \rightarrow t_{n+1}^{\mathcal{A}}$ for each function symbol $f \in F$.
- **Functional Programming**
- **Recursive Data Structures**



Stacks of Natural Numbers

$$\text{empty} : \rightarrow \text{stack}$$
$$\text{prefix} : \text{nat} \star \text{stack} \rightarrow \text{stack}$$
$$\text{first} : \text{stack} \rightarrow \text{nat}$$
$$\text{rest} : \text{stack} \rightarrow \text{stack}$$

$$\text{first}(\text{empty}) = \text{undefined}$$
$$\text{first}(\text{prefix}(n, s)) = n$$
$$\text{rest}(\text{empty}) = \text{undefined}$$
$$\text{rest}(\text{prefix}(n, s)) = s$$

Structural Induction

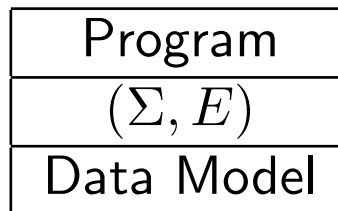
$$\frac{P[\text{empty}] \wedge P[s] \Rightarrow P[\text{prefix}(n, s)]}{\forall s : \text{stack} P[s]}$$



Data model \mathcal{S} for *stacks of natural numbers*

$$\begin{aligned} \text{stack}^{\mathcal{S}} &= \mathbb{N}^* \\ \text{empty}^{\mathcal{S}} &= \langle \rangle \\ \text{prefix}^{\mathcal{S}}(n, S) &= \langle n \rangle \& S \\ \text{first}^{\mathcal{S}}(\langle n_1, \dots, n_k \rangle) &= \begin{cases} \text{undefined} & \text{if } k = 0 \\ n_1 & \text{if } k \geq 1 \end{cases} \\ \text{rest}^{\mathcal{S}}(\langle n_1, \dots, n_k \rangle) &= \begin{cases} \text{undefined} & \text{if } k = 0 \\ \langle n_2, \dots, n_k \rangle & \text{if } k \geq 1 \end{cases} \end{aligned}$$

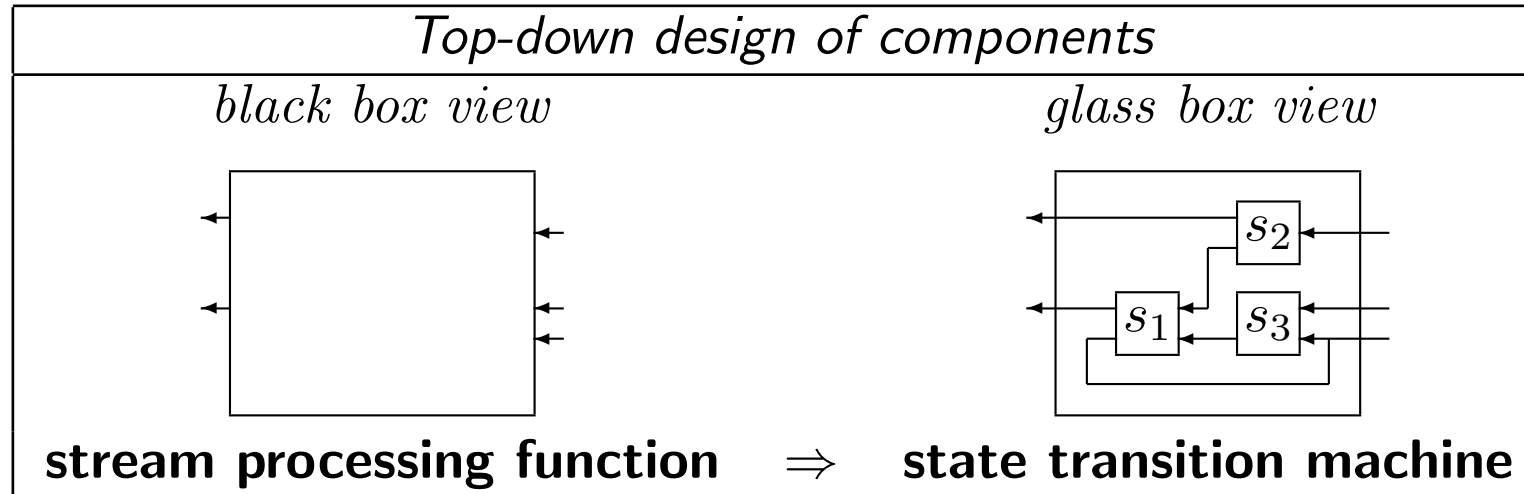
Vertical Modularization



No selective updating, sharing, pointers, . . .



3. Communication-Based Description: Black-box View



Black box view versus glass box view

- *Input/output Behaviour — Architecture*
- *Composition — Implementation*
- *Correctness — Efficiency*



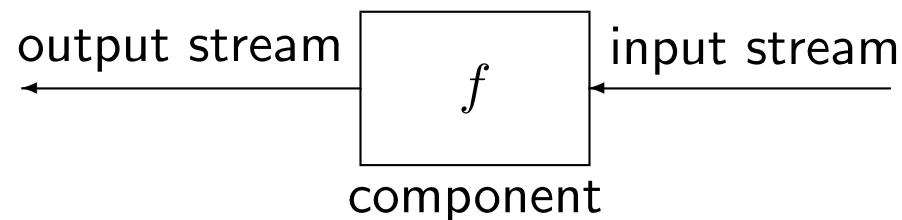
3.1 Streams and Stream Transformers

Streams model *communication histories on unidirectional channels*.

Finite streams $\mathcal{A}^* = \{\langle x_0, \dots, x_{m-1} \rangle \mid x_i \in \mathcal{A}, m \geq 0\}$

Concatenation $\langle x_0, \dots, x_{m-1} \rangle \ \& \ \langle y_0, \dots, y_{n-1} \rangle = \langle x_0, \dots, x_{m-1}, y_0, \dots, y_{n-1} \rangle$

Prefix relation $X \sqsubseteq Y$ iff $\exists R \in \mathcal{A}^* : X \ \& \ R = Y$
describes *operational progress in time*.



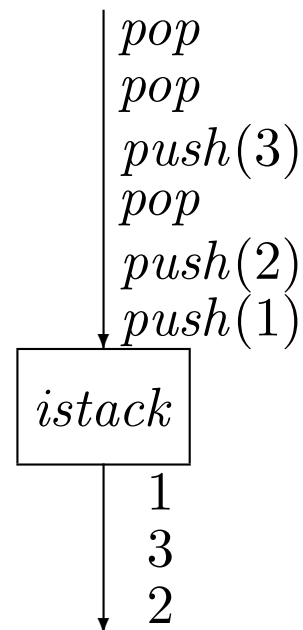
Stream transformer $f : \mathcal{A}^* \rightarrow \mathcal{B}^*$

Monotonicity $X \sqsubseteq Y \Rightarrow f(X) \sqsubseteq f(Y)$



3.2 Interactive Stack — Informal Description

An **interactive stack** stores an unbounded number of elements following a *last-in/first-out strategy*. The input consists of *push commands* entering a datum, and *pop commands* requesting the datum stored most recently.



3.3 Interactive Stack — Interface

Interaction Interface $(\mathcal{I}, \mathcal{O})$

type of input messages: $\mathcal{I} = \{pop, reset\} \cup push(nat)$

type of output messages: $\mathcal{O} = nat$

Transformation *Functional Signature* \mapsto *Interaction Interface*

$$\frac{prefix : nat \star stack \rightarrow stack}{push : nat \rightarrow [\underline{stack} \rightarrow \underline{stack}]}$$

$$\frac{\begin{array}{l} first : stack \rightarrow nat \\ rest : stack \rightarrow stack \end{array}}{pop : \underline{stack} \rightarrow nat \star \underline{stack}}$$

$$\frac{empty : \rightarrow stack}{reset : \underline{stack} \rightarrow \underline{stack}}$$

Design decisions about **encapsulation** (stack) and interaction interface.



3.4 Interactive Stack — Behaviour

$istack : \mathcal{I}^* \rightarrow \mathcal{O}^*$
<i>regular behaviour</i>
$istack(P) = \langle \rangle$
$istack(P \ \& \ \langle push(d), pop \rangle \ \& \ X) = d \triangleleft istack(P \ \& \ X)$
$istack(P \ \& \ \langle reset \rangle \ \& \ X) = istack(X)$
<i>irregular behaviour</i>
$istack(pop \triangleleft X) = \dots\dots$

- A sequence of *push* commands generates no output. $(P \in push(\mathbb{N})^*)$
- A *pop* command outputs the last datum not yet requested.
- After a *reset* command, the stack is empty.
- How to react on an unexpected *pop* command?



3.5 Interactive Stack — Irregular Behaviour

A **fault tolerant stack** *ignores* an *illegal request* from the *environment* and *continues* to perform its *service* on future input commands:

$$istack(pop \triangleleft X) = istack(X)$$

A **fault sensitive stack** *breaks* after an *illegal request* and *provides no output* whatever further input arrives. The output stems from the *longest regular prefix* of the irregular input history.

$$istack(pop \triangleleft X) = \langle \rangle$$

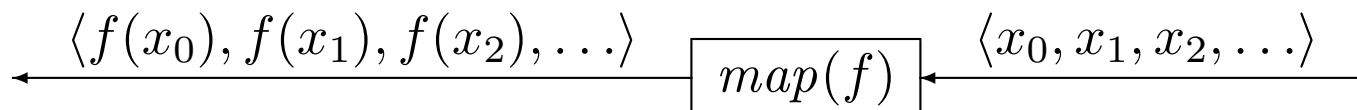
Further irregular behaviours: *Output a constant or suspend requests.*

Assumption/Guarantee Style

The *implementation* of the component *guarantees* the *specified behaviour* (service), only if the input streams *validate* the *assumptions* on the *environment*.



3.6 Iterator Component



An **iterator component** repeatedly applies a base function to all elements of the input stream.

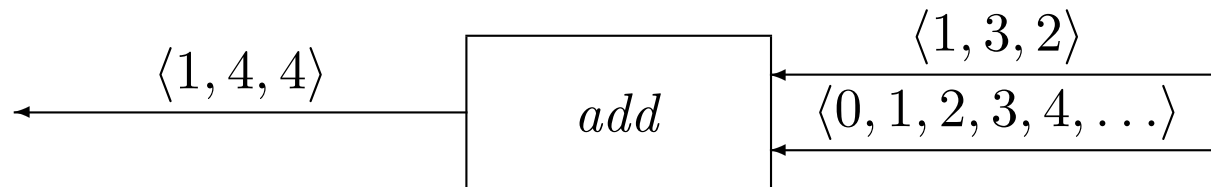
$map : [\mathcal{A} \rightarrow \mathcal{B}] \rightarrow [\mathcal{A}^* \rightarrow \mathcal{B}^*]$	
$map(f)(\langle \rangle)$	$= \langle \rangle$
$map(f)(x \triangleleft X)$	$= f(x) \triangleleft map(f)(X)$

The iterator component is *input/output synchronous* and *history insensitive*:

$$\begin{aligned} |map(f)(X)| &= |X| \\ map(f)(X)[i] &= f(X[i]) \quad (0 \leq i \leq |X| - 1) \end{aligned}$$



3.7 Adder Component



An **adder component** repeatedly calculates the sum of each two natural numbers arriving on the two input channels:

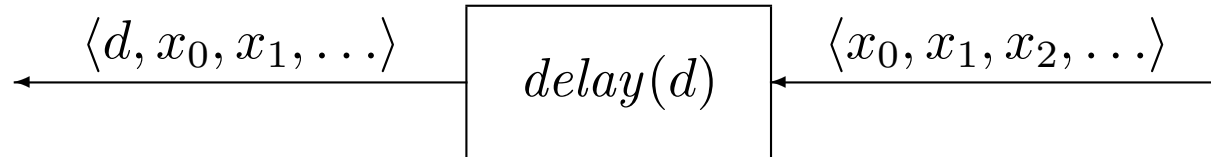
$add : \mathbb{N}^* \times \mathbb{N}^* \rightarrow \mathbb{N}^*$	
$ add(X, Y) $	$= \min(X , Y)$
$add(X, Y)[i]$	$= X[i] + Y[i] \quad (0 \leq i \leq add(X, Y) - 1)$

The adder component is *strict* (reactive) in both arguments:

$$add(\langle \rangle, Y) = \langle \rangle = add(X, \langle \rangle)$$



3.8 Delay Component



A **delay component** prefixes the input stream with an element:

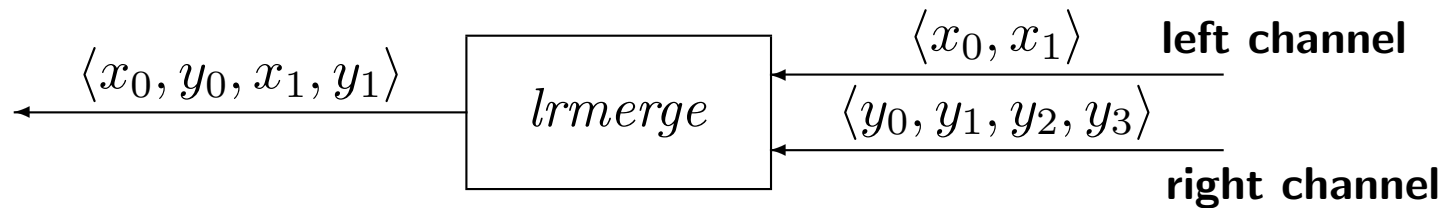
$delay : \mathcal{A} \rightarrow [\mathcal{A}^* \rightarrow \mathcal{A}^*]$
$delay(d)(\langle x_0, x_1, \dots, x_m \rangle) = \langle d, x_0, x_1, \dots, x_{m-1} \rangle$

The delay component is *input/output synchronous* and *history sensitive*:

$$\begin{aligned} |delay(d)(X)| &= |X| \\ delay(d)(X)[0] &= d \quad (|X| \geq 1) \\ delay(d)(X)[i+1] &= X[i] \quad (0 \leq i < |X| - 1) \end{aligned}$$



3.9 Deterministic Merge Component



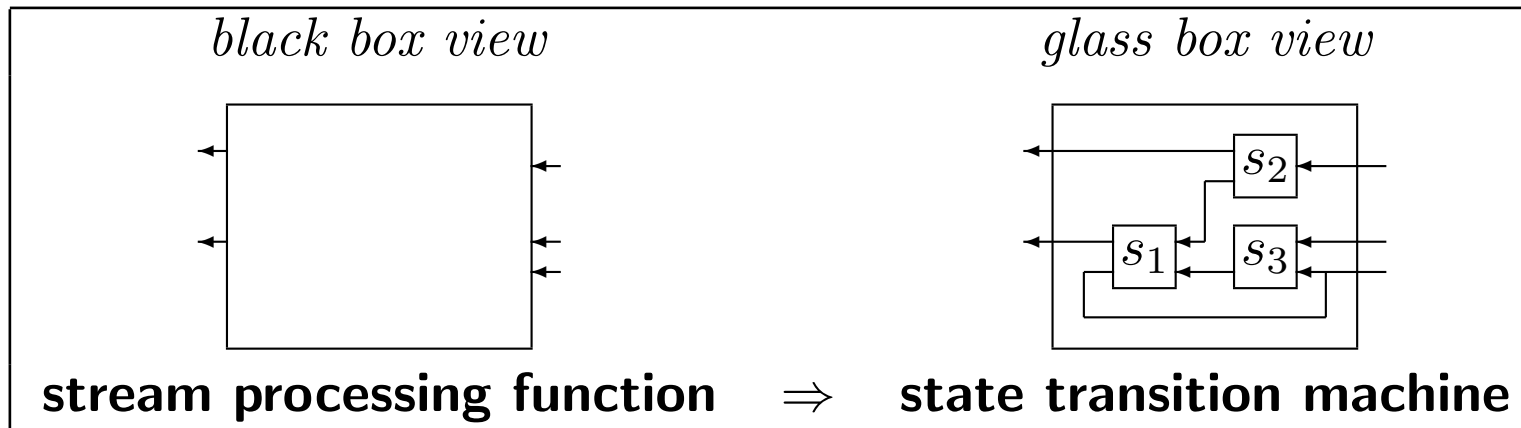
A **deterministic merge component** merges two communication streams in the following way: It first takes a message from the left channel, then a message from the right channel, and so on . . .

$lrmerge : \mathcal{A}^* \times \mathcal{B}^* \rightarrow (\mathcal{A} \cup \mathcal{B})^*$
$lrmerge(\langle \rangle, Y) = \langle \rangle$
$lrmerge(x \triangleleft X, Y) = x \triangleleft lrmerge(Y, X)$

The deterministic merge component is *history sensitive*. The *control state* must record the channel of the previous resp. the next message, the *data state* the elements stored.



4. State-Based Description: Glass-box View



The component is described by a (*collection of*) **communicating state transition machines with input and output.**



4.1 State Transition Machines

Constituents of the machine $M = (Q, \mathcal{I}, \mathcal{O}, \delta, \varphi, q_0)$

set Q of <i>states</i>	one-step <i>state transition function</i>	$\delta : Q \times \mathcal{I} \rightarrow Q$
set \mathcal{I} of <i>input data</i>	one-step <i>output function</i>	$\varphi : Q \times \mathcal{I} \rightarrow \mathcal{O}^*$
set \mathcal{O} of <i>output data</i>	<i>initial state</i>	$q_0 \in Q$

Processing input streams

multi-step <i>state transition function</i>	$\delta^* : Q \rightarrow [\mathcal{I}^* \rightarrow Q]$
multi-step <i>output function</i>	$\varphi^* : Q \rightarrow [\mathcal{I}^* \rightarrow \mathcal{O}^*]$

The multi-step output function $\varphi^*(q)$ is a stream transformer!



4.2 Interactive Stack — State Transition Machine

state space	$Q = \mathbb{N}^* \cup \{fail\}$
state transition function	$\delta(fail, x) = fail$ $\delta(Q, push(d)) = Q \& \langle d \rangle$ $\delta(Q, reset) = \langle \rangle$ $\delta(\langle \rangle, pop) = fail$ $\delta(Q \& \langle q \rangle, pop) = Q$
output function	$\varphi(S, push(d)) = \langle \rangle$ $\varphi(S, reset) = \langle \rangle$ $\varphi(fail, pop) = \langle \rangle$ $\varphi(\langle \rangle, pop) = \langle \rangle$ $\varphi(Q \& \langle q \rangle, pop) = \langle q \rangle$
initial state	$q_0 = \langle \rangle$



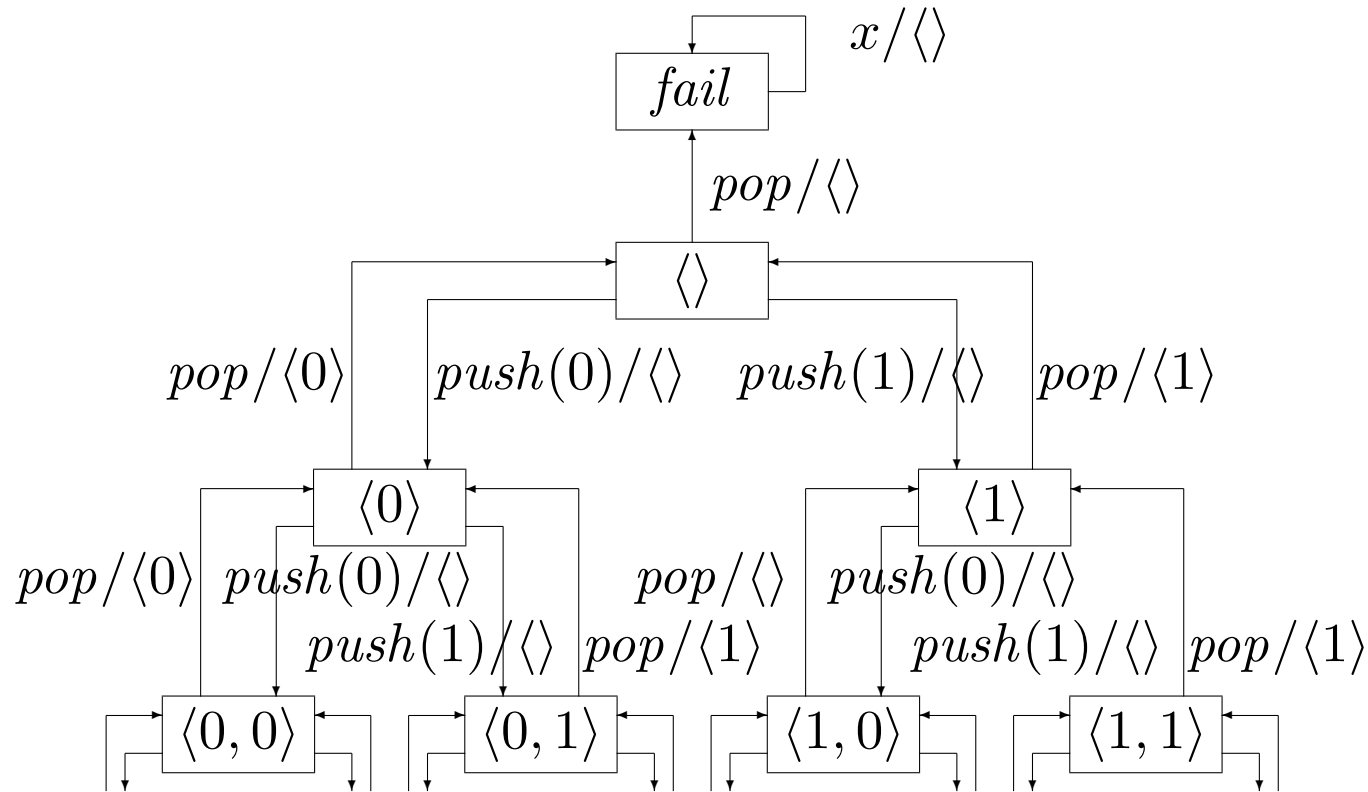
4.3 Interactive Stack — State Transition Table

Data State	Control State	Input	Data State'	Control State'	Output
	<i>fail</i>	<i>x</i>		<i>fail</i>	$\langle \rangle$
<i>Q</i>		<i>push(d)</i>	<i>Q</i> & $\langle d \rangle$		$\langle \rangle$
<i>Q</i>		<i>reset</i>	$\langle \rangle$		$\langle \rangle$
$\langle \rangle$		<i>pop</i>		<i>fail</i>	$\langle \rangle$
<i>Q</i> & $\langle q \rangle$		<i>pop</i>	<i>Q</i>		$\langle q \rangle$

A **state transition table** describes an *infinite state transition system* by a *finite number of transition rules*.



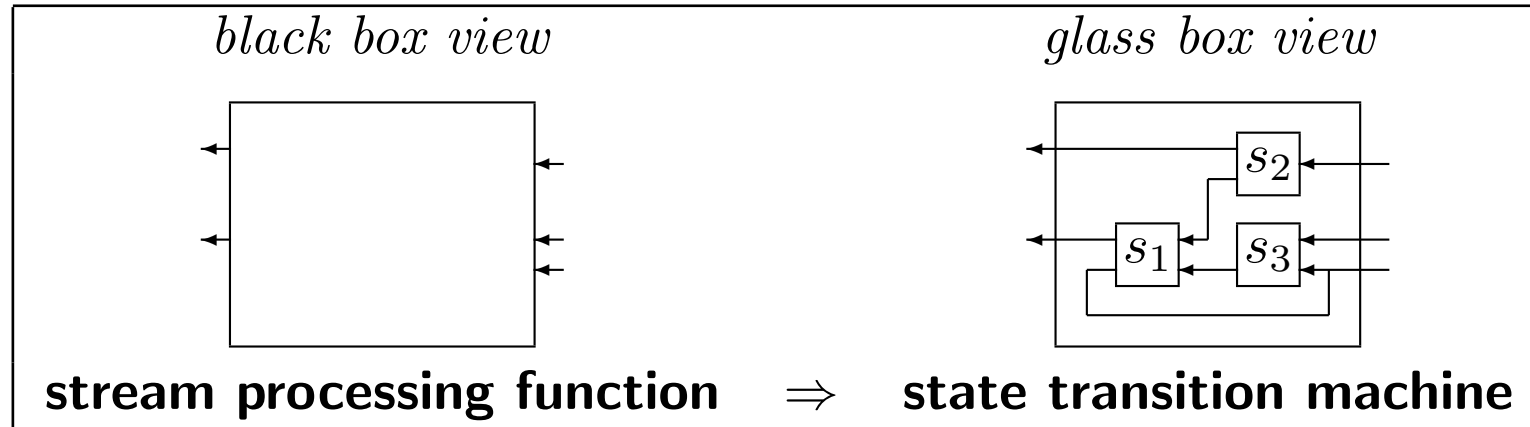
4.4 Interactive Stack — State Transition Diagram



Infinite double-linked tree with additional trap state (apart from *reset* arcs)



5. Differentiation and Abstraction



Differentiation *summary description* \mapsto *incremental description*.

$$\frac{f(X) \quad \text{diff}(f)(X, x)}{f(X \& \langle x \rangle)}$$

Differentiation

$$\frac{\text{diff} : [\mathcal{A}^* \rightarrow \mathcal{B}^*] \rightarrow [\mathcal{A}^* \times \mathcal{A} \rightarrow \mathcal{B}^*]}{\text{diff}(f)(X, x) = f(X \& \langle x \rangle) \ominus f(X)}$$

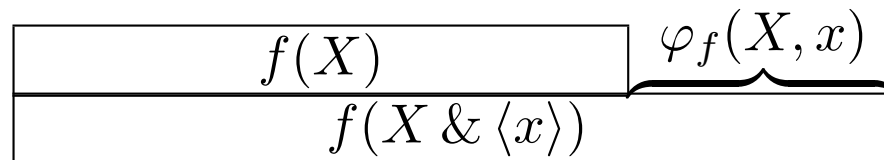


5.1 Canonical State Transition Machine

stream transformer \rightarrow **canonical state transition machine**

$$f : \mathcal{A}^* \rightarrow \mathcal{B}^* \quad \rightarrow \quad M_f = (\mathcal{A}^*, \mathcal{A}, \mathcal{B}, \delta, \varphi_f, \langle \rangle)$$

states	input histories	Q	$=$	\mathcal{A}^*
state transition function	extend input history	$\delta(X, x)$	$=$	$X \& \langle x \rangle$
output function	incremental output	$\varphi_f(X, x)$	$=$	$diff(f)(X, x)$
initial state	empty input history	q_0	$=$	$\langle \rangle$



Only the *output function* depends on the particular *stream transformer* f .



5.2. Abstraction

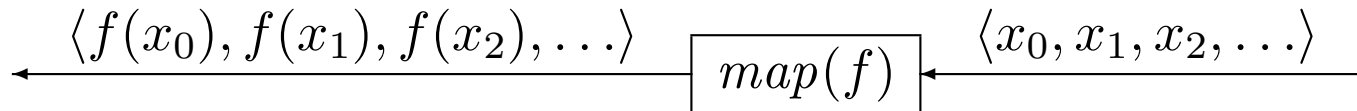
A **(state) abstraction function** *identifies* some states of state transition machine without changing the multi-step output function.

An *abstraction function* $abstr : Q \rightarrow Q'$ for a state transition machine $M = (Q, \mathcal{I}, \mathcal{O}, \delta, \varphi, q_0)$ is **transition closed** and **output compatible**

reduced state transition machine $M' = (Q', \mathcal{I}, \mathcal{O}, \delta', \varphi', q'_0)$	
set of states	Q'
state transition function	$\delta'(abstr(q), x) = abstr(\delta(q, x))$
output function	$\varphi'(abstr(q), x) = \varphi(q, x)$
initial state	$q'_0 = abstr(q_0)$



5.3 Iterator Component



An **iterator component** repeatedly applies a base function to all elements of the input stream.

Differential Description

$$\boxed{\text{diff}(\text{map}(f))(X, x) = \langle f(x) \rangle}$$

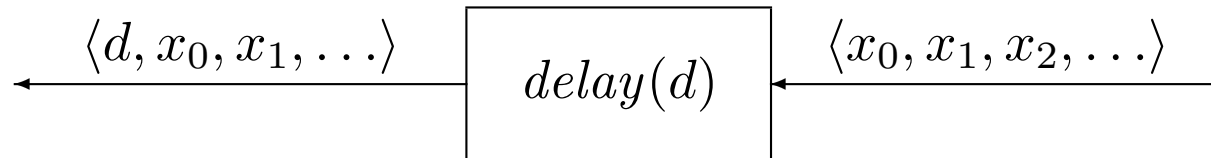
Abstraction

$$\boxed{\begin{array}{l} \text{abstr} : \mathcal{I}^* \rightarrow \{q_0\} \\ \text{abstr}(X) = q_0 \end{array}}$$

Component is *history insensitive*. \Leftrightarrow Implementation is *state free*.



5.4 Delay Component



A **delay component** prefixes the input stream with an element.

Differential Description

$$\begin{aligned} \text{diff}(\text{delay}(d))(\langle \rangle, y) &= \langle d \rangle \\ \text{diff}(\text{delay}(d))(X \& \langle x \rangle, y) &= \langle x \rangle \end{aligned}$$

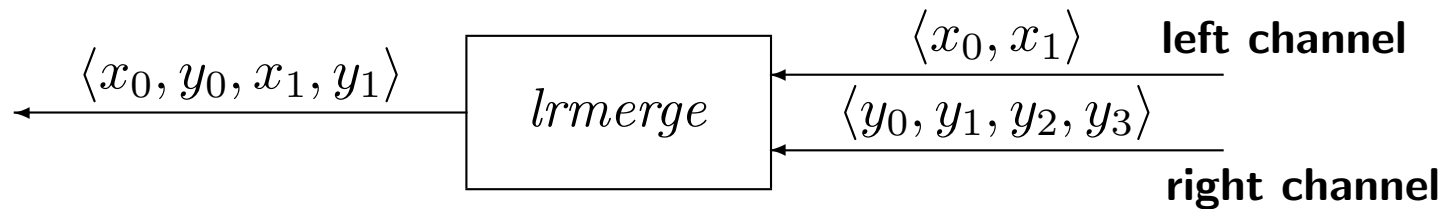
Abstraction

$$\begin{aligned} \text{abstr} : \mathcal{A}^* &\rightarrow \mathcal{A} \\ \text{abstr}(\langle \rangle) &= d \\ \text{abstr}(X \& \langle x \rangle) &= x \end{aligned}$$

Component is *history sensitive* — the state records the *last input*.



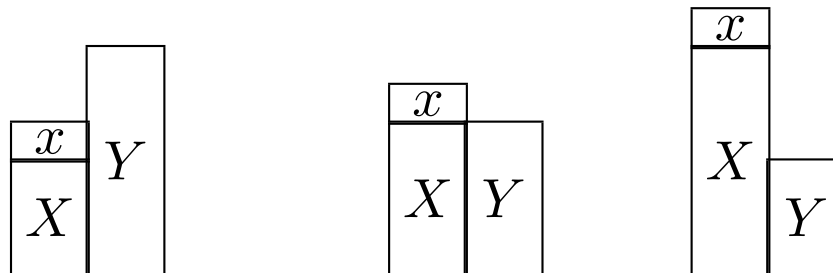
5.5 Deterministic Merge Component



The **deterministic merge component** merges two communication streams.

$$\text{diff}_1(\text{lrmerge})(X, Y)(x) = \begin{cases} \langle x, Y[|X|] \rangle & \text{if } |X| < |Y| \\ \langle x \rangle & \text{if } |X| = |Y| \\ \langle \rangle & \text{if } |X| > |Y| \end{cases}$$

Illustration



State Abstraction

$$Q = \underbrace{\{left, right\}}_{Control} \times \underbrace{(A^* \cup B^*)}_{Buffer}$$

State Transition Table

<i>Control</i>	<i>Buffer</i>	<i>Inleft</i>	<i>Inright</i>	<i>Control'</i>	<i>Buffer'</i>	<i>Out</i>
<i>left</i>	$\langle \rangle$	<i>a</i>	—	<i>right</i>	$\langle \rangle$	$\langle a \rangle$
<i>left</i>	$\langle r \rangle \& R$	<i>a</i>	—	<i>left</i>	<i>R</i>	$\langle a, r \rangle$
<i>right</i>	<i>L</i>	<i>a</i>	—	<i>right</i>	<i>L</i> & $\langle a \rangle$	$\langle \rangle$
<i>left</i>	<i>R</i>	—	<i>b</i>	<i>left</i>	<i>R</i> & $\langle b \rangle$	$\langle \rangle$
<i>right</i>	$\langle \rangle$	—	<i>b</i>	<i>left</i>	$\langle \rangle$	$\langle b \rangle$
<i>right</i>	$\langle l \rangle \& L$	—	<i>b</i>	<i>right</i>	<i>L</i>	$\langle b, l \rangle$

States are history abstractions.



5. Trace-Based Description: Process View

A **trace** records the *sequence of events* occurring during a *run* of the system.

Classification of Events

input event $?i$ (receiving a command i on the input channel)
output event $!o$ (sending a message o on the output channel)
internal event $q \mapsto q'$ (updating the internal state from q to q')

Set of Events

$$\mathcal{F} = \underbrace{(?I)}_{\text{input events}} \cup \underbrace{(!O)}_{\text{output events}} \cup \underbrace{(Q \mapsto Q')}_{\text{internal events}}$$

Input Traces $trace : Q \rightarrow [I^* \rightarrow \mathcal{F}^*]$

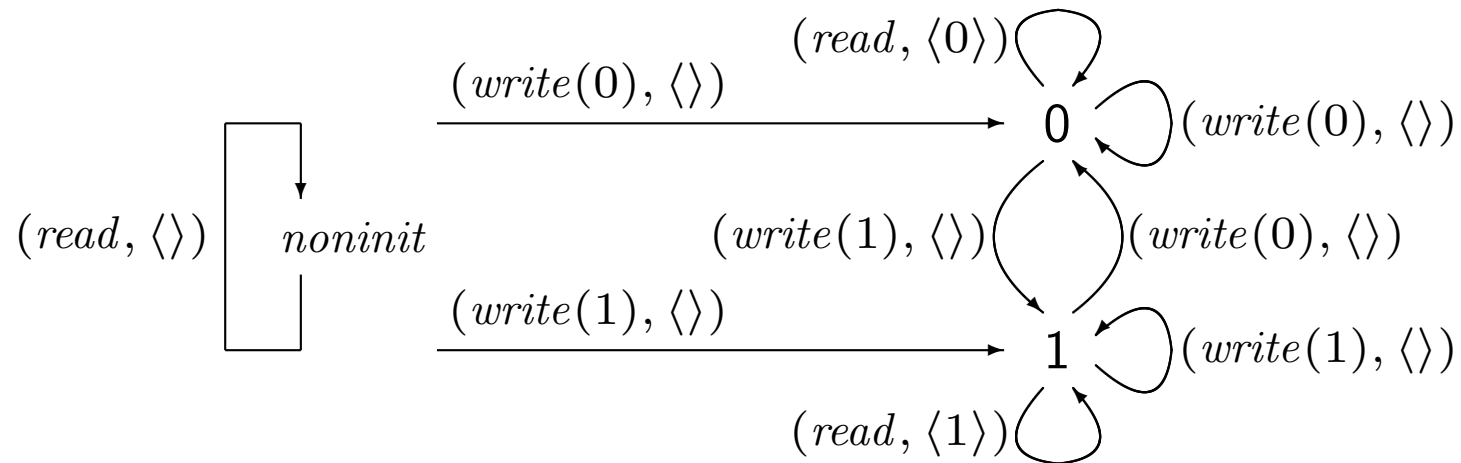
Trace Behaviour

$traces : Q \rightarrow \mathcal{P}(\mathcal{F}^*)$
$trace(q) = \{trace(q)(X) \mid X \in I^*\}$



Example: Binary Memory Cell

State transition diagram for a memory cell storing a binary data type $\{0, 1\}$



The memory cell is described by the **set of possible traces**:

$$traces : Q \rightarrow \mathcal{P}(\mathcal{F}^*)$$



$$\begin{aligned}
traces(noninit) &= \{\langle \rangle\} \\
&\cup \{?read\} \triangleleft traces(noninit) \\
&\cup \bigcup_{e \in \mathcal{D}} \{?write(e)\} \triangleleft \{(noninit \mapsto e)\} \triangleleft traces(e)
\end{aligned}$$

$$\begin{aligned}
traces(d) &= \{\langle \rangle\} \\
&\cup \{?read\} \triangleleft \{!d\} \triangleleft traces(d) \\
&\cup \bigcup_{e \in \mathcal{D}} \{?write(e)\} \triangleleft \{(d \mapsto e)\} \triangleleft traces(e)
\end{aligned}$$



KLEENE's fixpoint theorem establishes an *approximating chain* ($d \in \mathcal{D}$):

$$\text{traces}^{(0)}(d) = \emptyset$$

$$\text{traces}^{(1)}(d) = \{\langle \rangle\}$$

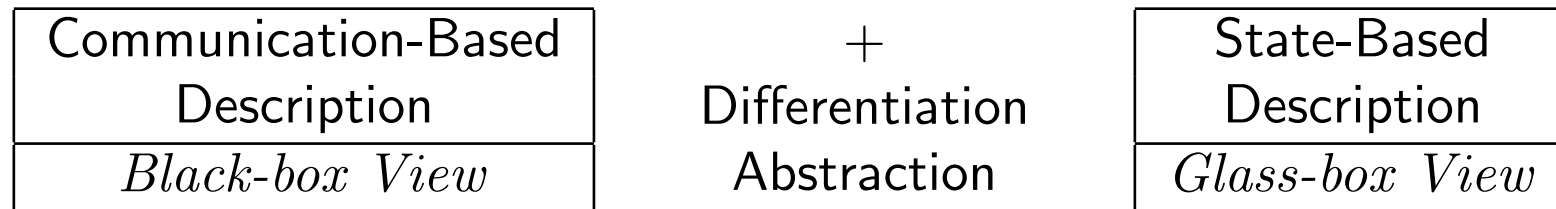
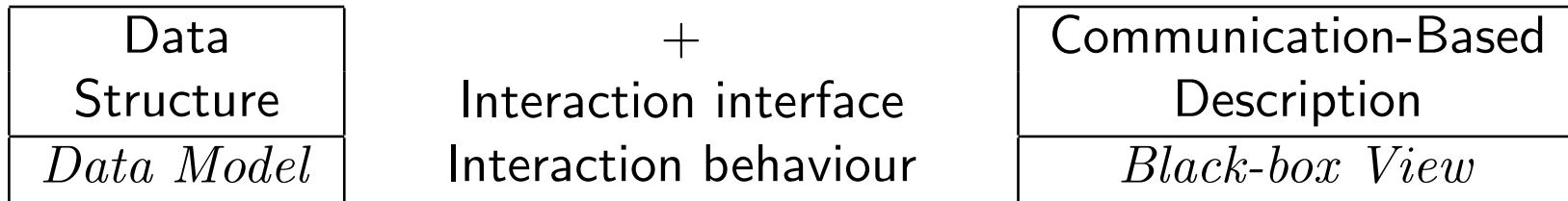
$$\begin{aligned} \text{traces}^{(2)}(d) = & \{\langle \rangle, \langle ?\text{read}, !d \rangle\} \cup \\ & \{\langle ?\text{write}(e), d \mapsto e \rangle \mid e \in \mathcal{D}\} \end{aligned}$$

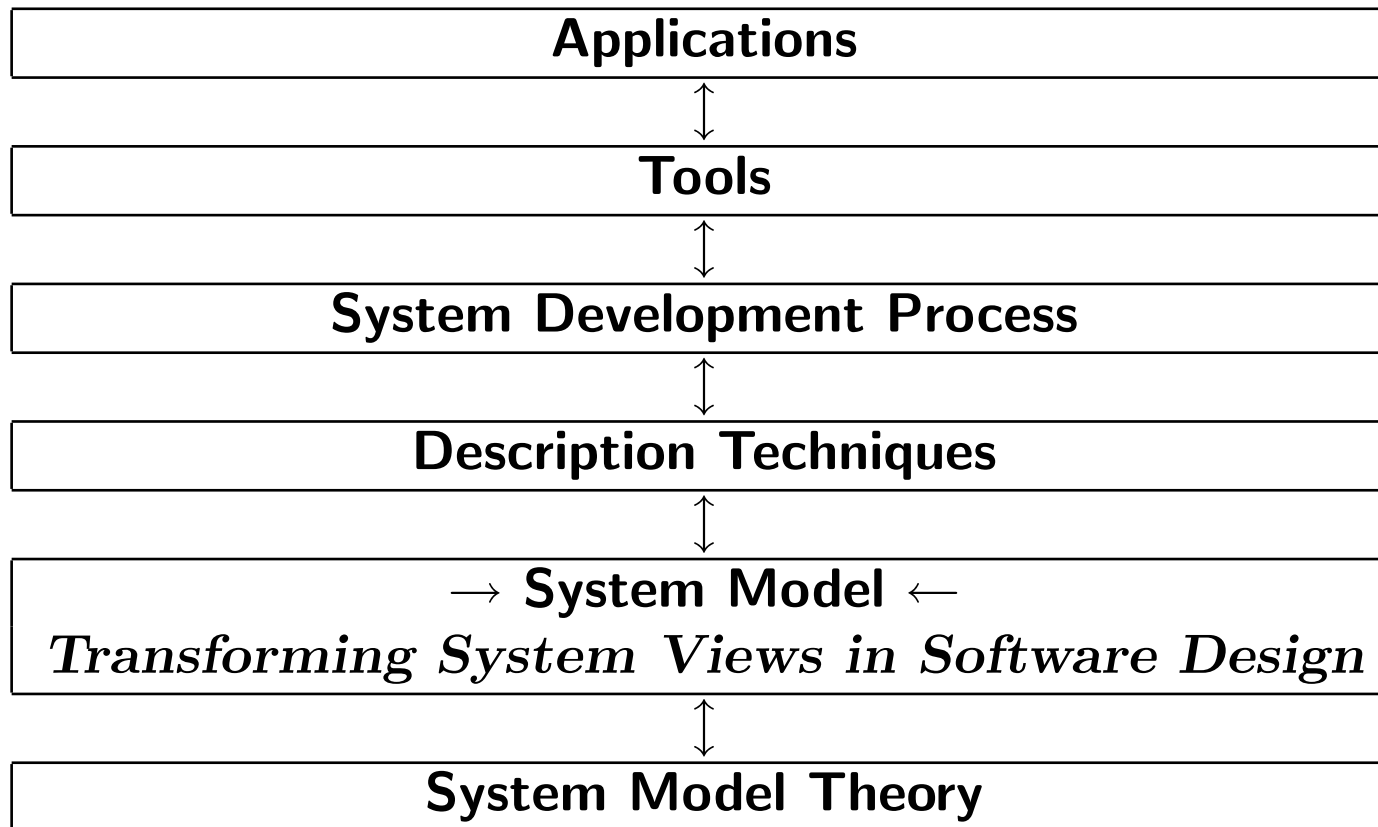
$$\begin{aligned} \text{traces}^{(3)}(d) = & \{\langle \rangle, \langle ?\text{read}, !d \rangle, \langle ?\text{read}, !d, ?\text{read}, !d \rangle\} \cup \\ & \{\langle ?\text{read}, !d, ?\text{write}(e), d \mapsto e \rangle, \\ & \langle ?\text{write}(e), d \mapsto e \rangle, \\ & \langle ?\text{write}(e), d \mapsto e, ?\text{read}, !e \rangle, \\ & \langle ?\text{write}(e), d \mapsto e, ?\text{write}(f), e \mapsto f \rangle \mid e, f \in \mathcal{D}\} \end{aligned}$$

⋮



7. Conclusion





Component-Based Systems		
layout	<i>static</i> topological	dynamic metric
communication	synchronous <i>unidirectional</i>	<i>asynchronous</i> bidirectional
state	<i>state-full</i> continuous <i>simple</i> shared	<i>state-less</i> <i>discrete</i> <i>structured</i> <i>distributed</i>
time	timed continuous sensitive	<i>untimed</i> discrete invariant
control	<i>(non)deterministic</i> centralized event-driven	stochastic <i>distributed</i> time-driven



Thanks for your Attention!

Any Questions, Amendments, Comments . . . ?

