

A Coq formalization of an analysis and optimization of While

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Introduction

- We present the formalization of the While language developed in Coq.
- Then we develop the type systems for live variable analysis and dead code elimination and checked them in Coq.
- We conducted the proofs constructively: prove the cases for the axioms as induction basis and use induction hypothesis for the compositional rules.

While language

$a ::= x \mid n \mid a_0 + a_1 \mid a_0 * a_1 \mid a_0 - a_1 \mid$

$b ::= a_0 = a_1 \mid tt \mid ff \mid \neg b \mid$

$s ::= x := a \mid skip \mid S_0; S_1 \mid \text{if } b \text{ then } S_0 \text{ else } S_1 \mid$
 $\text{while } b \text{ do } S.$

Arithmetic expressions

Inductive AExp : Set :=

| num : Num -> AExp

| var : Var -> AExp

| add : AExp -> AExp -> AExp

...

Fixpoint ASemFun (a : AExp) (s : State) {struct a} : nat :=

match a with

| num n => NumSemFun n

| var v1 => s v1

| add a1 a2 => (ASemFun a1 s) + (ASemFun a2 s)

...

end.

Boolean expressions

Inductive BExp : Set :=

- | ttrue : BExp
- | ffalse : BExp
- | aeq : AExp -> AExp -> BExp
- | neg : BExp -> BExp.

Fixpoint BSemFun (b : BExp) (s : State) {struct b} : bool :=

match b with

- | ttrue => true
- | ffalse => false
- | aeq a1 a2 => beq_nat (ASemFun a1 s) (ASemFun a2 s)
- | neg b => negb (BSemFun b s)

end.

Statements

Inductive $\text{Stm} : \text{Set} :=$

| $\text{ass} : \text{Var} \rightarrow \text{AExp} \rightarrow \text{Stm}$

| $\text{skip} : \text{Stm}$

| $\text{comp} : \text{Stm} \rightarrow \text{Stm} \rightarrow \text{Stm}$

| $\text{ifcond} : \text{BExp} \rightarrow \text{Stm} \rightarrow \text{Stm} \rightarrow \text{Stm}$

| $\text{whileloop} : \text{BExp} \rightarrow \text{Stm} \rightarrow \text{Stm}.$

Natural (big-step) semantics

Inductive eval : Stm -> State -> State -> Prop :=

| eval_ass : forall s x a,

eval (ass x a) s (update x a s)

...

| eval_comp : forall s1 s2 s3 S1 S2,

eval S1 s1 s2 ->

eval S2 s2 s3 ->

eval (comp S1 S2) s1 s3.

Natural (big-step) semantics

Inductive eval : Stm -> State -> State -> Prop :=

| eval_while_tt : forall s1 s2 s3 b S,

 true = (BSemFun b s1) ->

 eval S s1 s2 ->

 eval (whileloop b S) s2 s3 ->

 eval (whileloop b S) s1 s3

| eval_while_ff : forall s b S,

 false = (BSemFun b s) ->

 eval (whileloop b S) s s.

Hoare logic

Definition $\text{Cond} := \text{State} \rightarrow \text{Prop}$. (*Conditions (Pre- and Post-)*)

(* Hoare Triple *)

Inductive $\text{infer_triple} : \text{Cond} \rightarrow \text{Stm} \rightarrow \text{Cond} \rightarrow \text{Prop} :=$

| $\text{infer_triple_ass} : \text{forall } P \ x \ a,$
 $\text{infer_triple } (\text{condAss } P \ x \ a) \ (x \leftarrow a) \ P$

...

| $\text{infer_triple_comp} : \text{forall } P \ Q \ R \ S1 \ S2,$
 $\text{infer_triple } P \ S1 \ Q \rightarrow$
 $\text{infer_triple } Q \ S2 \ R \rightarrow$
 $\text{infer_triple } P \ (\text{comp } S1 \ S2) \ R$

Hoare logic

Inductive infer_triple : Cond -> Stm -> Cond -> Prop :=

...

| infer_triple_while : forall P b S,
 infer_triple (boolConj b P) S P ->
 infer_triple P (whileloop b S) (boolConj (neg b) P)

...

(*P – loop invariant*)

Soundness and completeness

Definition `valid_triple` ($P : \text{Cond}$) ($S : \text{Stm}$) ($Q : \text{Cond}$) :=
forall $s\ s'$, $P\ s \wedge (\text{eval } S\ s\ s') \rightarrow Q\ s'$.

Theorem `HoareLogicSoundness` :
forall ($P\ Q : \text{Cond}$) ($S : \text{Stm}$),
`infer_triple P S Q` $\rightarrow$ `valid_triple P S Q`.

Theorem `HoareLogicCompleteness` :
forall ($P : \text{Cond}$) ($S : \text{Stm}$) ($Q : \text{Cond}$),
`valid_triple P S Q` $\rightarrow$ `infer_triple P S Q`.

Live variables analysis

Definition $\text{Live} := \text{Var} \rightarrow \text{bool}$.

Definition $\text{add} (v : \text{Var}) (\text{live} : \text{Live}) : \text{Live} :=$
 $\text{fun } (v' : \text{Var}) =>$
 $\text{if beq_nat } v \ v' \text{ then true else live } v'.$

Definition $\text{subset} (\text{live0} : \text{Live}) (\text{live1} : \text{Live}) : \text{Prop} :=$
 $\text{forall } (v : \text{Var}),$
 $\text{live0 } v = \text{true} \rightarrow \text{live1 } v = \text{true}.$

Live variables analysis

Inductive type_live_aexp : AExp -> Live -> Live -> Prop :=

| type_live_var : forall live x0,
type_live_aexp (var x0) (add x0 live) live

...

Inductive type_live_stm : Stm -> Live -> Live -> Prop

| type_live_ass_in_live : forall live live' x a0,
member x live' = true ->
type_live_aexp a0 live (remove x live') ->
type_live_stm (ass x a0) live live'

| type_live_ass_not_live : forall live x a0,
member x live = false ->
type_live_stm (ass x a0) live live

...

Dead code elimination

Inductive type_dead_stm : Stm -> Live -> Live -> **Stm ->** Prop :=

| type_dead_ass_in_live : forall live live' x a0,
 member x live' = true ->
 type_live_aexp a0 live (remove x live') ->
 type_dead_stm (ass x a0) live live' **(ass x a0)**

| type_dead_ass_not_live : forall live x a0,
 member x live = false ->
 type_dead_stm (ass x a0) live live **(skip)**

...

Dead code elimination

Definition related (s0 s1 : State) (live : Live) :=
forall (v : Var), member v live = true -> s0 v = s1 v.

Theorem DCE_Soundness_a :

forall (S : Stm) (live live' : Live) (S' : Stm),
type_dead_stm S live live' S' ->
forall s0 s1, related s0 s1 live ->
forall s0', eval S s0 s0' ->
exists s1', related s0' s1' live' /\ eval S' s1 s1'.

Theorem DCE_Soundness_b :

forall (S : Stm) (live live' : Live) (S' : Stm),
type_dead_stm S live live' S' ->
forall s0 s1, related s0 s1 live ->
forall s1', eval S' s1 s1' ->
exists s0', related s0' s1' live' /\ eval S s0 s0'.

Conclusion

DCE	...	...
LVA	...	...
Hoare logic		
Operational semantics		
Syntax		

Conclusion

- The modularity consists of formalization of syntax as first abstract level, operational semantics as second level and Hoare logic as third level.
- Live variable analysis as the next level. Its type system is sound in the big-step semantics and Hoare logic.
- Basing on the fourth-level type system we developed another one for the dead code elimination.
- We leave the constructed environment for future development of an optimization tools. For instance, the common subexpression elimination based on the available expressions analysis will be added soon.

Thank you!

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